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April 1st, 2010 Renesas Electronics Corporation

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DATA SHEET



MOS FIELD EFFECT TRANSISTOR

μ PA2792GR

SWITCHING N- AND P-CHANNEL POWER MOS FET

DESCRIPTION

The μ PA2792GR is N- and P-channel MOS Field Effect Transistors designed for Motor Drive application.

FEATURES

· Low on-state resistance

N-channel R_{DS(on)1} = 12.5 m Ω MAX. (V_{GS} = 10 V, I_D = 5 A)

 $R_{DS(on)2} = 21 \text{ m}\Omega \text{ MAX.} \text{ (Vgs = 4.5 V, ID = 5 A)}$

P-channel R_{DS(on)1} = 18 m Ω MAX. (V_{GS} = -10 V, I_D = -5 A)

 $R_{DS(on)2} = 26 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = -4.5 \text{ V, I}_D = -5 \text{ A)}$

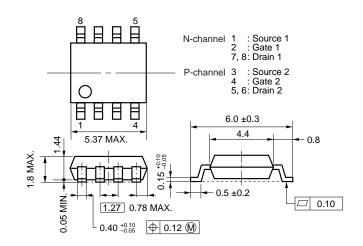
• Low input capacitance

N-channel Ciss = 2200 pF TYP.

P-channel Ciss = 2200 pF TYP.

- · Built-in gate protection diode
- Small and surface mount package (Power SOP8)

PACKAGE DRAWING (Unit: mm)

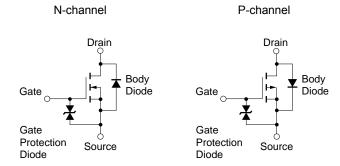


ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE		
μPA2792GR-E1-AZ Note		Tape 2500 p/reel			
μPA2792GR-E2-AZ ^{Note}	PA2792GR-E2-AZ Note		Power SOP8		

Note Pb-free (This product does not contain Pb in external electrode).

EQUIVALENT CIRCUITS



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD.

When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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ABSOLUTE MAXIMUM RATINGS (TA = 25°C. All terminals are connected.)

PARAMETER	SYMBOL	N-CHANNEL	P-CHANNEL	UNIT	
Drain to Source Voltage (V _{GS} = 0 V)	VDSS	30	-30	V	
Gate to Source Voltage (VDS = 0 V)	V _{GSS}	±20	∓20	V	
Drain Current (DC)	I _{D(DC)}	±10	∓10	А	
Drain Current (pulse) Note1	I _{D(pulse)}	±40	∓40	А	
Total Power Dissipation (1 unit) Note2	P _{T1}	1.7		W	
Total Power Dissipation (2 units) Note2	P _{T2}	2	W		
Channel Temperature	Tch	150		°C	
Storage Temperature	Tstg	−55 to +150		°C	
Single Avalanche Current Note3	las	10	-10	А	
Single Avalanche Energy Note3	Eas	,	mJ		

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

- 2. Mounted on ceramic substrate of 2000 mm² x 1.6 mm
- 3. Starting Tch = 25°C, Vdd = 15 V, Rg = 25 Ω , L = 100 μ H, Vgs = 20 \rightarrow 0 V



ELECTRICAL CHARACTERISTICS (TA = 25°C. All terminals are connected.)

N-channel

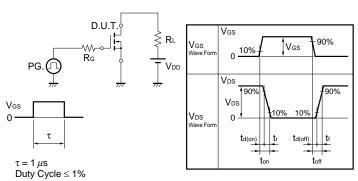
N-Chainlei						
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 30 V, V _{GS} = 0 V			10	μΑ
Gate Leakage Current	Igss	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μΑ
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	٧
Forward Transfer Admittance Note	yfs	V _{DS} = 10 V, I _D = 5 A	5	10		S
Drain to Source On-state Resistance Note	R _{DS(on)1}	V _{GS} = 10 V, I _D = 5 A		10	12.5	mΩ
	R _{DS(on)2}	V _{GS} = 4.5 V, I _D = 5 A		14.5	21	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V,		2200		pF
Output Capacitance	Coss	V _{GS} = 0 V,		380		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		250		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 15 V, I _D = 5 A,		9.6		ns
Rise Time	tr	V _{GS} = 10 V,		21		ns
Turn-off Delay Time	td(off)	$R_G = 0 \Omega$		52		ns
Fall Time	tr			12		ns
Total Gate Charge	Q _G	I _D = 10 A,		42		nC
Gate to Source Charge	Qgs	V _{DD} = 24 V,		6.2		nC
Gate to Drain Charge	Q _{GD}	V _{GS} = 10 V		13		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = 10 A, V _{GS} = 0 V		0.83	1.5	V
Reverse Recovery Time	trr	I _F = 10 A, V _{GS} = 0 V,		30		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		22		nC

Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

$V_{GS} = 20 \rightarrow 0 \text{ V}$ V_{DD} V_{DD} D.U.T. V_{DD} V_{DD} V_{DD} V_{DD} V_{DD} V_{DD}

TEST CIRCUIT 2 SWITCHING TIME



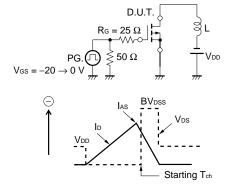
TEST CIRCUIT 3 GATE CHARGE

P-channel

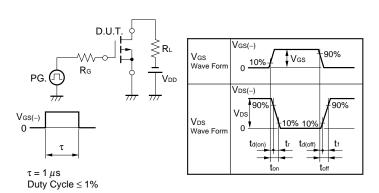
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = -30 V, V _{GS} = 0 V			-10	μΑ
Gate Leakage Current	Igss	V _{GS} = ∓20 V, V _{DS} = 0 V			∓10	μΑ
Gate to Source Cut-off Voltage	V _{GS(off)}	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-1.0	-1.7	-2.5	V
Forward Transfer Admittance Note	yfs	V _{DS} = -10 V, I _D = -5 A	6	12.9		S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = -10 V, I _D = -5 A		14	18	mΩ
	R _{DS(on)2}	V _{GS} = -4.5 V, I _D = -5 A		17.5	26	mΩ
Input Capacitance	Ciss	V _{DS} = -10 V,		2200		pF
Output Capacitance	Coss	V _{GS} = 0 V,		510		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		410		pF
Turn-on Delay Time	t _{d(on)}	$V_{DD} = -15 \text{ V}, I_D = -5 \text{ A},$		12		ns
Rise Time	tr	V _{GS} = -10 V,		19		ns
Turn-off Delay Time	t _{d(off)}	R _G = 0 Ω		130		ns
Fall Time	tf			36		ns
Total Gate Charge	Q _G	I _D = -10 A,		47		nC
Gate to Source Charge	Q _{GS}	V _{DD} = -24 V,		5.2		nC
Gate to Drain Charge	Q _{GD}	V _{GS} = -10 V		15		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = 10 A, V _{GS} = 0 V		0.87	1.5	V
Reverse Recovery Time	trr	I _F = -10 A, V _{GS} = 0 V,		57		ns
Reverse Recovery Charge	Qrr	di/dt = -50 A/μs		41		nC

Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY



TEST CIRCUIT 2 SWITCHING TIME

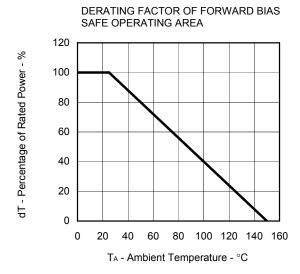


TEST CIRCUIT 3 GATE CHARGE

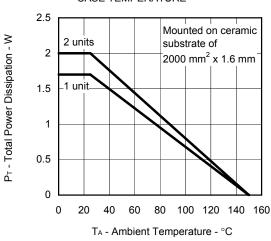
$$\begin{array}{c|c} D.U.T. \\ \hline \\ IG = -2 \text{ mA} \\ \hline \\ PG. \\ \hline \\ \end{array} \begin{array}{c} RL \\ \hline \\ V_{DD} \\ \hline \end{array}$$

TYPICAL CHARACTERISTICS (TA = 25°C)

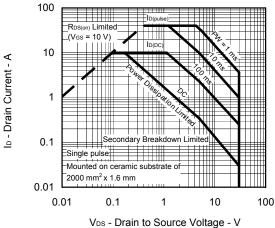
(1) N-channel



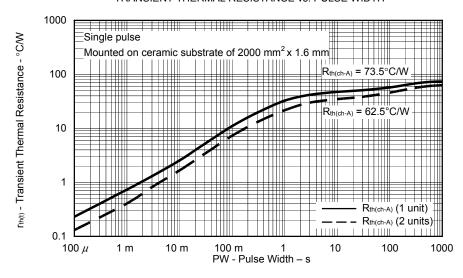
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



FORWARD BIAS SAFE OPERATING AREA



TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

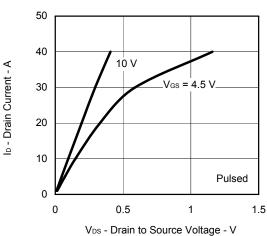


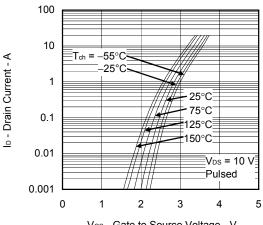
Vos(off) - Gate to Source Cut-off Voltage - V

R_{DS(m)} - Drain to Source On-state Resistance - mΩ

6



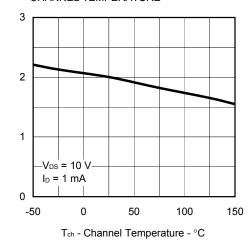




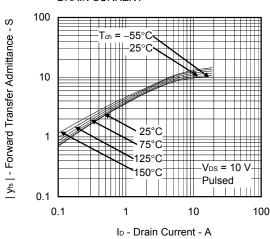
FORWARD TRANSFER CHARACTERISTICS

V_{GS} - Gate to Source Voltage - V

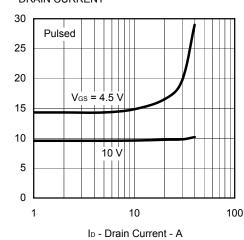
GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



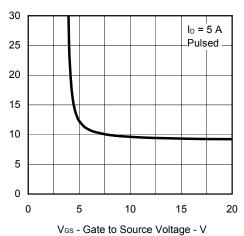
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

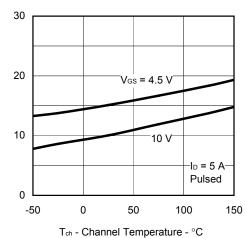


 $\mathsf{R}_{\mathsf{DS}(m)}$ - Drain to Source On-state Resistance - $m\Omega$

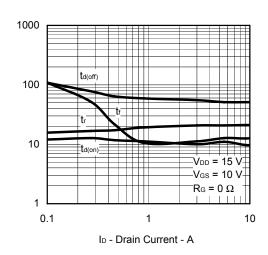
 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - $m\Omega$

td(on), tr, td(off), tr - Switching Time - ns

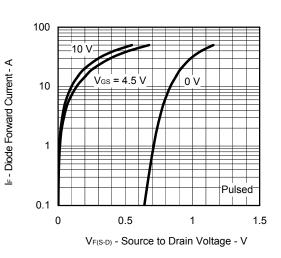




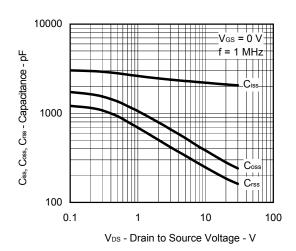
SWITCHING CHARACTERISTICS



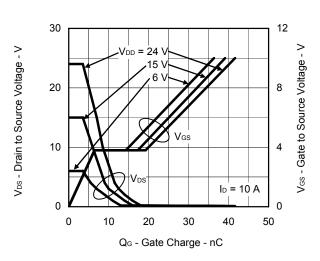
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



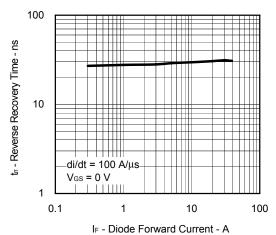
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



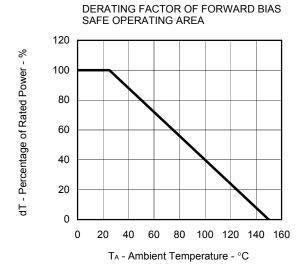
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



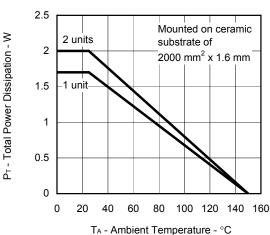
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



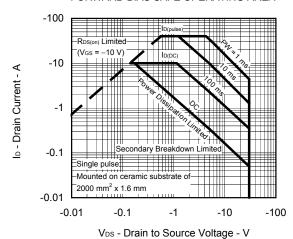
(2) P-channel

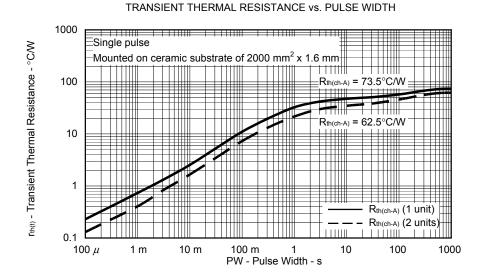


TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



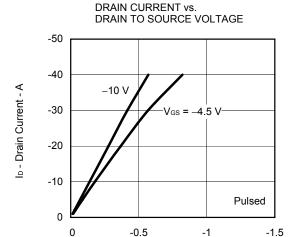
FORWARD BIAS SAFE OPERATING AREA





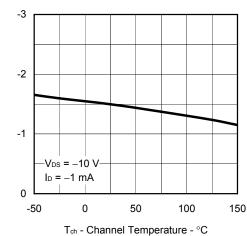
Vos(off) - Gate to Source Cut-off Voltage - V

R_{DS(m)} - Drain to Source On-state Resistance - mΩ

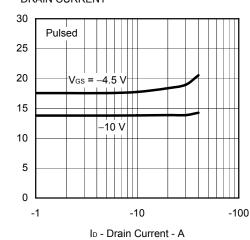


GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE

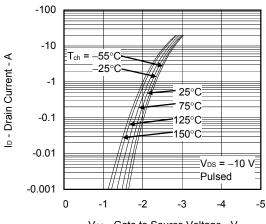
VDS - Drain to Source Voltage - V



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

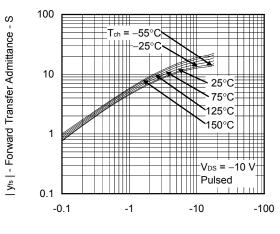


FORWARD TRANSFER CHARACTERISTICS



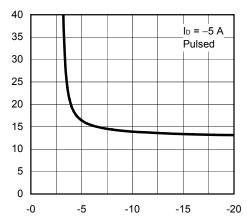
V_{GS} - Gate to Source Voltage - V

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



ID - Drain Current - A

DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



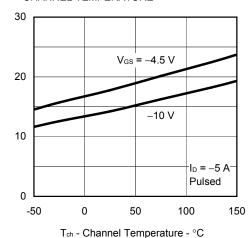
V_{GS} - Gate to Source Voltage - V

 $\mathsf{R}_{\mathsf{DS}(m)}$ - Drain to Source On-state Resistance - $m\Omega$

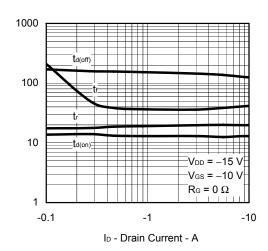
 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - $m\Omega$

td(on), fr, td(off), fr - Switching Time - ns

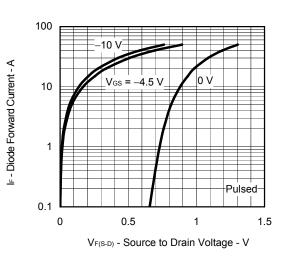
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



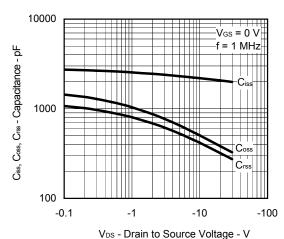
SWITCHING CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE

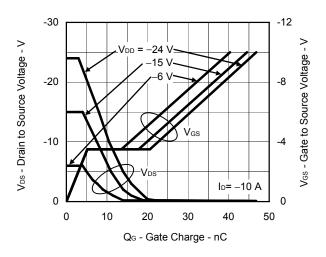


CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

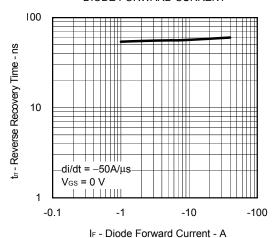


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DYNAMIC INPUT/OUTPUT CHARACTERISTICS



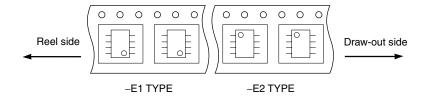
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



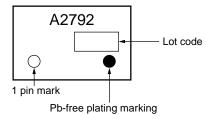
IF - Diode i diward Current -

TAPE INFORMATION

There are two types (-E1, -E2) of taping depending on the direction of the device.



MARKING INFORMATION



RECOMMENDED SOLDERING CONDITIONS

The μ PA2792GR should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, please contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Infrared reflow	Maximum temperature (Package's surface temperature): 260°C or below	IR60-00-3
	Time at maximum temperature: 10 seconds or less	
	Time of temperature higher than 220°C: 60 seconds or less	
	Preheating time at 160 to 180°C: 60 to 120 seconds	
	Maximum number of reflow processes: 3 times	
	Maximum chlorine content of rosin flux (percentage mass): 0.2% or less	
Partial heating	Maximum temperature (Pin temperature): 350°C or below	P350
	Time (per side of the device): 3 seconds or less	
	Maximum chlorine content of rosin flux: 0.2% (wt.) or less	

Caution Do not use different soldering methods together (except for partial heating).

 μ PA2792GR

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- While NEC Electronics endeavors to enhance the quality, reliability and safety of NEC Electronics products, customers agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize risks of damage to property or injury (including death) to persons arising from defects in NEC Electronics products, customers must incorporate sufficient safety measures in their design, such as redundancy, fire-containment and anti-failure features.
- NEC Electronics products are classified into the following three quality grades: "Standard", "Special" and "Specific".
 - The "Specific" quality grade applies only to NEC Electronics products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of an NEC Electronics product depend on its quality grade, as indicated below. Customers must check the quality grade of each NEC Electronics product before using it in a particular application.
 - "Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots.
 - "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support).
 - "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC Electronics products is "Standard" unless otherwise expressly specified in NEC Electronics data sheets or data books, etc. If customers wish to use NEC Electronics products in applications not intended by NEC Electronics, they must contact an NEC Electronics sales representative in advance to determine NEC Electronics' willingness to support a given application.

(Note)

- (1) "NEC Electronics" as used in this statement means NEC Electronics Corporation and also includes its majority-owned subsidiaries.
- (2) "NEC Electronics products" means any product developed or manufactured by or for NEC Electronics (as defined above).